

	L #	Hits	Search Text	DBs
1	L1	902	(713/300).CCLS.	USPAT; USOCR
2	L2	196094	power near3 consum\$5	USPAT; EPO; DERWENT
3	L3	16785	12 with lower\$3	USPAT; EPO; DERWENT
4	L5	25	13 same (respon\$4 near5 (low near2 power))	USPAT
5	L6	5	13 same (respon\$4 near5 (low near2 voltage))	USPAT
6	L7	22	13 same (detect\$4 near5 (low near2 power))	USPAT
7	L8	4	13 same (determin\$5 near5 (low near2 power))	USPAT
8	L9	6	13 same (detect\$4 near5 (low near2 voltage))	USPAT
9	L10	1	13 same (determin\$5 near5 (low near2 voltage))	USPAT
10	L11	38670	(power near3 consum\$5) with reduc\$4	USPAT
11	L12	94040	(power near3 consum\$5) with reduc\$4	USPAT; EPO; DERWENT
12	L13	13	112 same (respon\$4 near5 (low near2 voltage))	USPAT; EPO; DERWENT
13	L14	71	112 same (respon\$4 near5 (low near2 power))	USPAT; EPO; DERWENT
14	L15	87	112 same (detect\$4 near5 (low near2 power))	USPAT; EPO; DERWENT
15	L16	37	112 same (detect\$4 near5 (low near2 voltage))	USPAT; EPO; DERWENT
16	L17	12	112 same (determin\$5 near5 (low near2 voltage))	USPAT; EPO; DERWENT
17	L18	24	112 same (determin\$5 near5 (low near2 power))	USPAT; EPO; DERWENT
18	L19	17066	switch\$3 near3 processor	USPAT; EPO; DERWENT
19	L20	3	119 same (respon\$4 near5 (low near2 voltage))	USPAT; EPO; DERWENT
20	L21	6	119 same (respon\$4 near5 (low near2 power))	USPAT; EPO; DERWENT
21	L22	15200	chang\$3 near5 processor	USPAT; EPO; DERWENT
22	L23	2	122 same (detect\$4 near5 (low near2 power))	USPAT; EPO; DERWENT

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23	L24	4	l22 same (respon\$4 near5 (low near2 power))	USPAT; EPO; DERWENT
24	L25	2	l22 same (detect\$4 near5 (low near2 voltage))	USPAT; EPO; DERWENT
25	L26	7	l22 same (determin\$5 near5 (low near2 voltage))	USPAT; EPO; DERWENT
26	L27	0	l22 same (determin\$5 near5 (low near2 power))	USPAT; EPO; DERWENT
27	L28	0	transfer\$4 near2 control same low\$1voltage near3 processor	USPAT; EPO; DERWENT
28	L29	131670	chang\$3 near5 process\$3	USPAT; EPO; DERWENT
29	L30	1	l29 same (respon\$4 near5 (low near2 voltage))	USPAT; EPO; DERWENT
30	L31	182	transfer\$4 near2 control same low near2 voltage	USPAT; EPO; DERWENT
31	L32	81885	transfer\$4 near3 control	USPAT; EPO; DERWENT
32	L33	10	l32 same (respon\$4 near5 (low near2 voltage))	USPAT; EPO; DERWENT
33	L34	3	l32 same (respon\$4 near5 (low near2 power))	USPAT; EPO; DERWENT
34	L35	23	low-power adj1 condition	USPAT; EPO; DERWENT
35	L36	23	low\$1power adj1 condition	USPAT; EPO; DERWENT
36	L37	107	low\$1voltage adj1 condition	USPAT; EPO; DERWENT
37	L38	122	low\$1voltage adj1 state	USPAT; EPO; DERWENT
38	L39	231	low\$1power adj1 state	USPAT; EPO; DERWENT
39	L40	18	(low\$1power adj1 state) with respon\$4	USPAT; EPO; DERWENT
40	L41	15	(low\$1power adj1 state) with detect\$3	USPAT; EPO; DERWENT
41	L42	100	(monitor\$3 near3 power) same (communication adj1 device\$1)	USPAT; EPO; DERWENT
42	L43	408	(monitor\$3 near3 power) same backup	USPAT; EPO; DERWENT

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43	L44	0	(monitor\$3 near3 power) same (backup adj1 processor)	USPAT; EPO; DERWENT
44	L45	14	(monitor\$3 near3 power) same (second adj1 processor)	USPAT; EPO; DERWENT
45	L46	31680	(monitor\$3 near3 voltage)	USPAT; EPO; DERWENT
46	L47	31	l46 same (communication adj1 device\$1)	USPAT; EPO; DERWENT
47	L48	303	l46 same backup	USPAT; EPO; DERWENT
48	L49	9	l46 same (second adj1 (processing or processor))	USPAT; EPO; DERWENT